

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,845,050 B2  
DATED : January 18, 2005  
INVENTOR(S) : Jae Jin Lee

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10,

Line 2, after "load element" please delete "is PMOS" and insert -- is a PMOS -- in its place.

Line 53, after "impedance circuits" please delete "comprises;" and insert -- comprises: -- in its place.

Signed and Sealed this

Fifth Day of July, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*